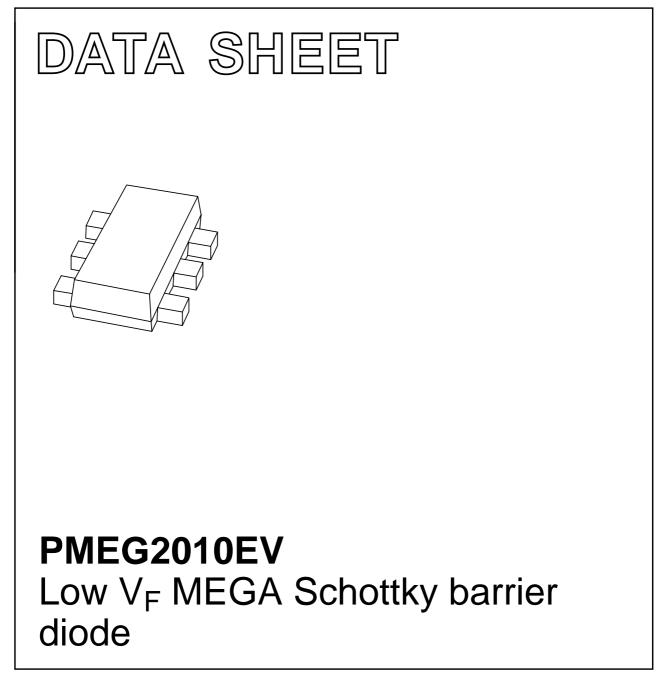
DISCRETE SEMICONDUCTORS



Preliminary specification

2002 Jun 24



Low V_F MEGA Schottky barrier diode

FEATURES

- Forward current: 1 A
- Reverse voltage: 20 V
- Very low forward voltage
- Ultra small SMD package
- Flat leads: excellent coplanarity and improved thermal behaviour.

APPLICATIONS

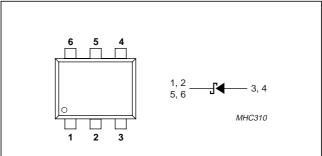
- Low voltage rectification
- High efficiency DC/DC conversion
- Switch mode power supply
- Inverse polarity protection
- Low power consumption applications.

DESCRIPTION

Planar Maximum Efficiency General Application (MEGA) Schottky barrier diode with an integrated guard ring for stress protection in a SOT666 ultra small SMD plastic package.

PINNING

PIN	DESCRIPTION	
1	cathode	
2	cathode	
3	anode	
4	anode	
5	cathode	
6	cathode	



Marking code: F1.

Fig.1 Simplified outline (SOT666) and symbol.

LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134)

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V _R	continuous reverse voltage		-	20	V
l _F	continuous forward current		-	1	A
I _{FSM}	non-repetitive peak forward current	t = 8.3 ms half sinewave; JEDEC method; note 1	-	8	A
T _{stg}	storage temperature		-65	+150	°C
Tj	junction temperature		-	125	°C
T _{amb}	operating ambient temperature		-65	+125	°C

2

Note

1. Only valid, if pins 3 and 4 are connected in parallel.

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THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R _{th j-a}	thermal resistance from junction to ambient	note 1	405	K/W
		note 2	215	K/W

Notes

- 1. Refer to SOT666 standard mounting conditions.
- 2. Mounted on printed circuit-board, 1 cm² copper area.

Soldering

The only recommended soldering method is reflow soldering.

ELECTRICAL CHARACTERISTICS

 $T_{amb} = 25 \ ^{\circ}C$ unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	TYP.	MAX.	UNIT
V _F	continuous forward voltage	I _F = 10 mA	240	270	mV
		I _F = 100 mA	300	350	mV
		I _F = 1000 mA; note 1; see Fig.2	480	550	mV
I _R	reverse current	$V_R = 5 V$; note 2	5	10	μA
		$V_R = 8 V$; note 2	7	20	μA
		V _R = 15 V; note 2; see Fig.3	10	50	μA
C _d	diode capacitance	$V_R = 5 V$; f = 1 MHz; see Fig.4	19	25	pF

Notes

1. Only valid if pins 1, 2, 5 and 6 are soldered on a 1 cm^2 copper solder land.

2. Pulse test: $t_p = 300 \ \mu s$; $\delta = 0.02$.

Low V_F MEGA Schottky barrier diode

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MHC312

(1)

(2)

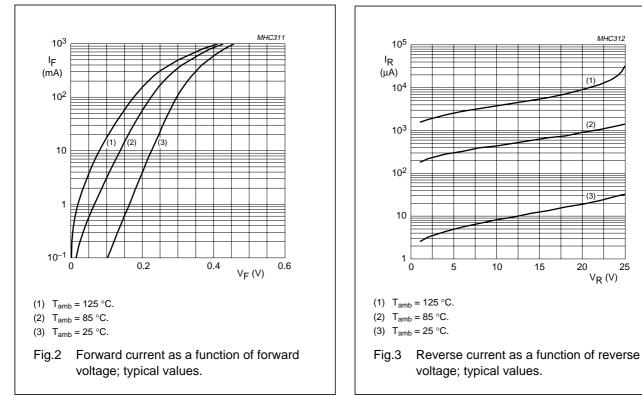
(3)

20

25

 $V_{\mathsf{R}}(V)$

GRAPHICAL DATA



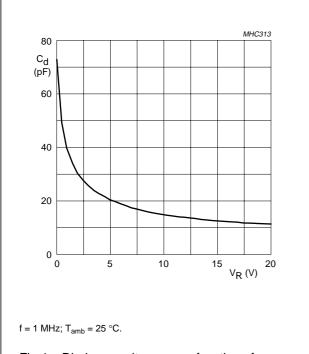
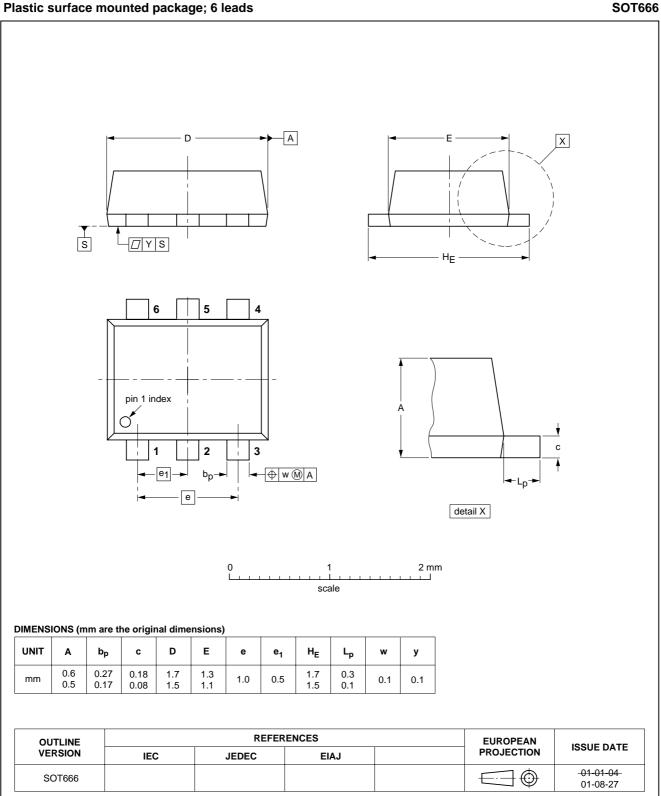


Fig.4 Diode capacitance as a function of reverse voltage; typical values.

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PACKAGE OUTLINE



Low V_F MEGA Schottky barrier diode

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DATA SHEET STATUS

DATA SHEET STATUS ⁽¹⁾	PRODUCT STATUS ⁽²⁾	DEFINITIONS
Objective data	Development	This data sheet contains data from the objective specification for product development. Philips Semiconductors reserves the right to change the specification in any manner without notice.
Preliminary data	Qualification	This data sheet contains data from the preliminary specification. Supplementary data will be published at a later date. Philips Semiconductors reserves the right to change the specification without notice, in order to improve the design and supply the best possible product.
Product data	Production	This data sheet contains data from the product specification. Philips Semiconductors reserves the right to make changes at any time in order to improve the design, manufacturing and supply. Changes will be communicated according to the Customer Product/Process Change Notification (CPCN) procedure SNW-SQ-650A.

Notes

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- 2. The product status of the device(s) described in this data sheet may have changed since this data sheet was published. The latest information is available on the Internet at URL http://www.semiconductors.philips.com.

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NOTES

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